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				APPLICANT: Xavier Robert			
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Sheet	1	of	1				

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
HIEN VO <small>Digitally signed by HIEN VO DN: cn = HIEN VO, c = US, ou = 2863 Reason: I have reviewed this document Date: 2006.09.28 17:59:55 -04'00'</small>		5,724,505		Argade et al.	03/03/1998
		5,848,264		Baird et al.	12/08/1998
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Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
HIEN VO		EP	1184790	A	Mann	03/06/02	

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
HIEN VO		International Search Report from corresponding International Application No. PCT/FR2002/003996, filed November 21, 2002	
		International Search report from a related International Application No. PCT/FR2002/003908, filed November 14, 2002	
		International Search report from a related International Application No. PCT/FR2002/003909, filed November 14, 2002	
		Nexis 5001 Forum: <u>Standard for a Global Embedded Processor Debug Interface</u> , December 15, 1999, IEEE-ISTO XP002247195	

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*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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